Docket No.: 60188-03 PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Kentaro SHIOMI, et al.

Serial No.: 09/779,440

Group Art Unit:

Filed: February 09, 2001

Examiner:

For:

LSI DESIGN METHOD AND VERIFICATION METHOD

TRANSMITTAL OF FORMAL DRAWINGS

Commissioner for Patents Washington, DC 20231

Sir:

At the time the above application was filed, informal drawings were presented with the application.

The formal drawings are submitted herewith.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

Michael E F garty

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Date: July 18, 2001

Facsimile: (202) 756-8087

Docket No.: 60188-031

PATENT

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In re Application of

Kentaro SHIOMI, et al.

Serial No.: 09/779,440

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Examiner:

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LSI DESIGN METHOD AND VERIFICATION METHOD

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents Washington, DC 20231

Sir:

Attached is a copy of the Filing Receipt received from the U.S. Patent and Trademark Office in the above-referenced application. It is noted that the number of total and independent claims is incorrect on the Filing Receipt. Please correct the number of total claims to reflect 23 and the number of independent claims to reflect 7. It is requested that a corrected filing receipt be issued.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

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Date: July 18, 2001

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COMMISSIONER FOR PATENTS UNITED STATES PATENT AND TRADEMARK OFFICE WASHINGTON D.C. 2023L

APPLICATION NUMBER

FILING DATE GRP ART UNIT

FIL FEE REC'D ATTY. DOCKET, NO DRAWINGS

www.uspta.gov IND CLAIMS

09/779,440

02/09/2001

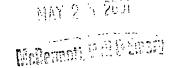
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60188-031

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CONFIRMATION NO. 6677

MCDERMOTT WILL & EMERY 600 13TH STREET, N.W. WASHINGTON, DC 20005-3096



FILING RECEIPT OC000000006093211*

Date Mailed: 05/18/2001

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER. FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Kentaro Shiomi, Residence Not Provided; Akira Motohara, Residence Not Provided: Makoto Fujiwara, Residence Not Provided; Toshiyuki Yokoyama, Residence Not Provided; Katsuya Fujimura, Residence Not Provided;

Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2000-034577 02/14/2000

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Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

Title

LSI design method and verification method